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Dated: 10/7/03 Signature: Mae Pattison
(Mae Pattison)

Docket No.: 299002056600
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Ken SUMITANI

Application No.: 10/612,319

Confirmation No.: 4067

Filed: July 1, 2003

Art Unit: 2824

For: SEMICONDUCTOR MEMORY DEVICE

Examiner: Not Yet Assigned

REQUEST FOR CORRECTED FILING RECEIPT

Office of Initial Patent Examination's
Filing Receipt Corrections
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicant, a copy of which is attached hereto, has the following errors:

Under the Applicant(s) section please replace the following:

"Nara" with --Tenri-shi--.

Also enclosed is a Supplemental Application Data Sheet showing the correct city name.

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

In the unlikely event that the transmittal letter is separated from this document and the Patent Office determines that an extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Assistant Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 03-1952**, referencing Docket No. 299002056600. However, the Assistant Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: October 7, 2003

Respectfully submitted,

By 

Alan S. Hodes

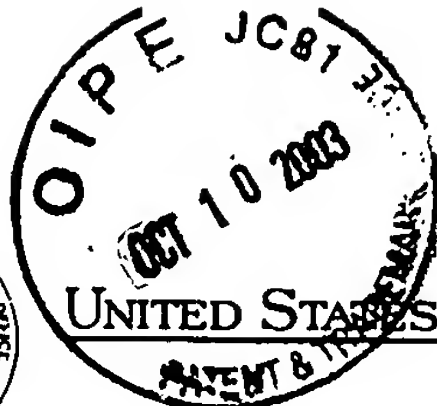
Registration No.: 38,185

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/612,319	07/01/2003	2824	750	299002056600	13	7	1

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Morrison & Foerster, LLP
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CONFIRMATION NO. 4067

FILING RECEIPT



OC000000010935478

Date Mailed: 09/26/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Tenri-shi
 Ken Sumitani, ~~Nara~~^{Nara}, JAPAN;

Assignment For Published Patent Application

SHARP KABUSHIKI KAISHA, Osaka, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-195025 07/03/2002

If Required, Foreign Filing License Granted: 09/26/2003

Projected Publication Date: 01/08/2004

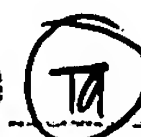
Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor memory device

DOCKETED



Preliminary Class

365

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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